

ABSTRACT OF THE DISCLOSURE

A programmable logic module. In the programmable logic module, a first printed circuit board has a socket and a downloading unit. A field programmable gate array (FPGA) is disposed on the first printed circuit board. A nonvolatile memory stores program codes for programming the field programmable gate array. The nonvolatile memory is soldered to a second printed circuit board with a plurality of pins corresponding to the socket, and the second printed circuit board is plugged into the socket of the first printed circuit board. The nonvolatile memory downloads program codes thereof to the field programmable gate array by the downloading unit.